

(Use several sheets if necessary)

OKA-C06

09-808,016

MAEDA et al

March 15, 2001

GROUP

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
LAR		6-267939	9/22/94	Japan				
LAR		1 058 301 A1	2/12/99	Europe				
LAR		9-205089	8/5/97	Japan				
LAR		5-343394	12/24/93	Japan				
LAR		9-148324	6/6/97	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

ATTY DOCKET NO.

OKA-C062

SERIAL NO.

09-808,016

APPLICANT(S)

MAEDA et al

FILING DATE

March 15, 2001

GROUP

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CK	5,656,337	8/12/97	Park et al	437	238	—
CK	5,470,800	5/26/93	Muroyama	427	539	—
WU	5,424,253	6/13/95	Usami	437	238	—
WU	5,605,867	2/25/97	Sato et al	437	235	—

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
WU	7-50295	2/21/95	Japan	—	—	—	—
WU	5-259297	10/8/93	Japan	—	—	—	—
WU	7-99191	4/11/95	Japan	—	—	—	—
WU	7-122552	5/12/95	Japan	—	—	—	—
WU	7-29901	1/31/95	Japan	—	—	—	—

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

WU			"Silicon Processing for the VLSI Era, Volume 2 - Process Integration. By Stanley. Wolf PhD. pp 196-199

EXAMINER

WU

DATE CONSIDERED

4/16/02

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Patent and Trademark Office * U.S. DEPARTMENT OF COMMERCE